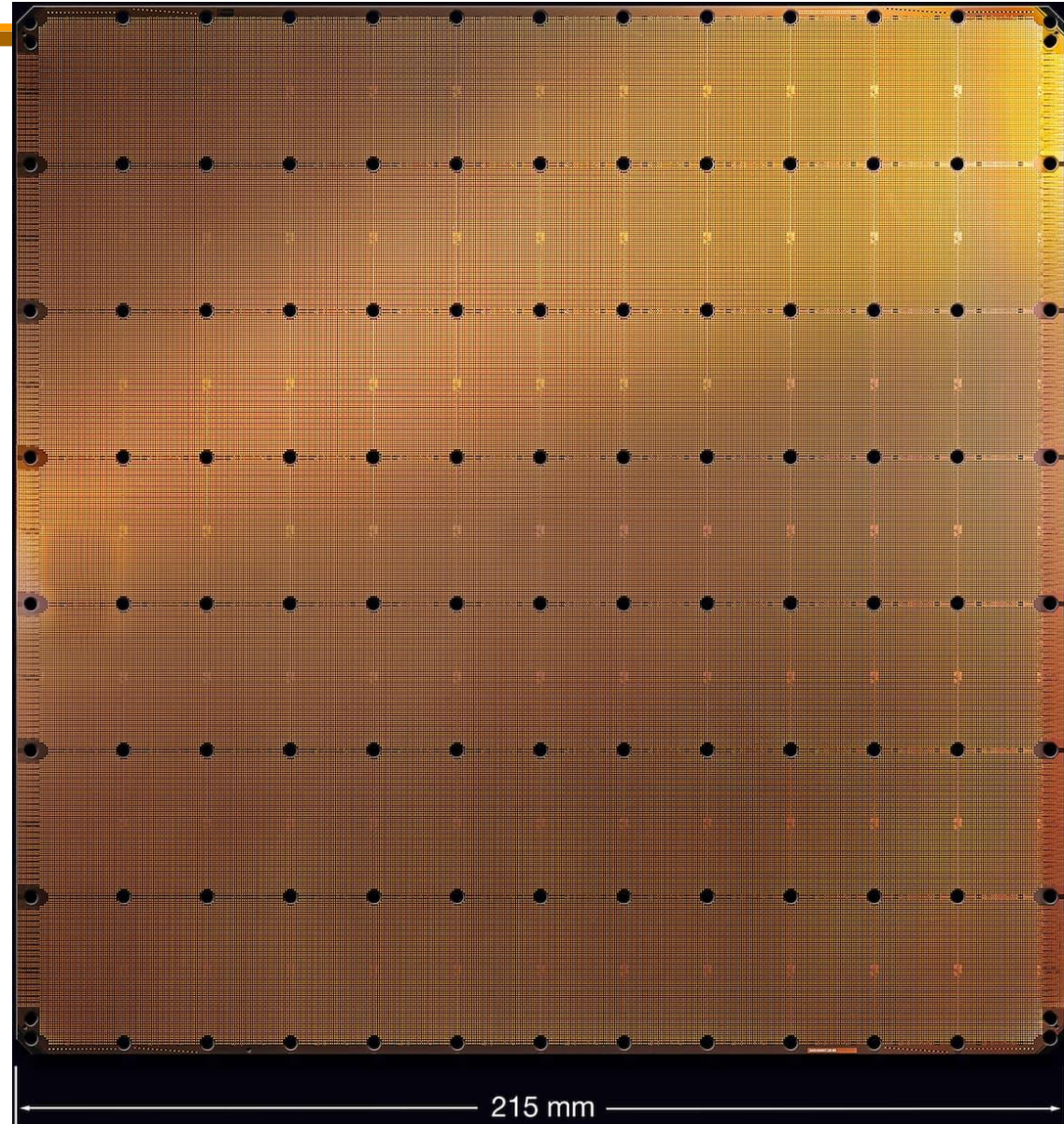


# The World's Largest Chip

## Cerebras Wafer-Scale Engine

- 46,225 mm<sup>2</sup> chip
  - 8.5" × 8.5"
  - Built from a 12" wafer
  - 56x larger than the biggest GPU ever made: 815 mm<sup>2</sup> and 21.1 billion transistors
- 1.2 Trillion transistors
- 15 KWatts!
- 400,000 cores
- Fabled by TSMC, 98%-99% of wafer area is usable
- 18 GB on-chip SRAM
- 100 Pb/s interconnect (100,000 Tb/s = 12,500 TB/sec)
- Approximately \$200M startup capital as of Aug 2019



# Cerebras WSE-2 (Second Generation)

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- 46,225 mm<sup>2</sup> chip
- 2.6 Trillion transistors
- 850,000 cores
- 40 GB on-chip SRAM
- 220 Pb/s interconnect (100,000 Tb/s = 12,500 TB/sec)

