Figure 4.1 Single-bus organization of the data paths inside the CPU.
Figure 4.6 Two-bus structure.
Figure 4.7 Three-bus structure.
**FIGURE 10-5.** Register organization extensions for 32-bit microprocessors
FIGURE 10-3. Simplified Intel 8085 organization.
Instruction Formats (example):

**Load/Store:**

- LD
- RT
- RB
- DISPL

**Operation**

- ADD
- RT
- RS1
- RS2

**Branch**

- BR
- Address